

Manuel Hermenegildo

Areas of Interest

Advanced Program Development Technology, Global Program Analysis / Optimization / Verification, Abstract Interpretation, Abstract Machines, Parallelizing Compilers, Resource-Aware Distributed Execution and Logical Agents, Automatic Documentation Tools, Constraint/Logic/Functional Programming Theory and Implementation, Execution Visualization, Sequential and Parallel Computer Architecture.

Education

- Aug 1986 **PhD.**, Electrical and Computer Eng., University of Texas at Austin.
Aug 1984 **M. S.**, Electrical and Computer Eng., University of Texas at Austin.
Jun 1981 **M. S.**, Electrical Eng., Technical U. of Madrid.

Positions

- Jan 2007-... **Scientific Director**, *Madrid Institute for Advanced Research in Software Development Technology (IMDEA-Software, www.software.imdea.org); Madrid, Spain, and*
- Jan 1990-... **Full Professor** (January 1990-May 1994 **Associate Professor**), *Technical U. of Madrid, Department of Computer Science; Spain.*
General scientific coordination of the Institute. Research. Graduate and undergraduate teaching. PhD. student supervision. Leader of the CLIP (Computational Logic, Implementation and Parallelism) Lab. Principal investigator in several EU, international, and national projects on automatic parallelization, verification, abstraction carrying code, global analysis of programs, constraint logic programming, implementation of concurrent constraint languages, program debugging, automatic Web-site generation, next generation program development environments, and others.
- Feb 03-Dec 08 **Prince of Asturias Endowed Chair** in Information Science and Technology, *U. of New Mexico, Departments of Computer Science and Electrical and Computer Engineering; Albuquerque, NM, USA*
Research. Graduate and undergraduate teaching. PhD. student supervision. International student supervision under exchange programs.
- May 00-Sep 02 **Director** of the *Spanish National Research Directorate* (Spanish central agency financing research in all areas of science and technology).
Creation of the directorate from a number of previously existing units. Design and management of all programs funding projects, human resources, and infrastructures, including starting the "Ramon y Cajal" fellowship program. Coordination with regional, European, and international programs. Benchmarking of the national S&T system. Member, board of directors of the Spanish Foundation for Science and Technology, the Spanish Scientific Research Council (CSIC), and the Spanish Center for Industrial and Technological Development (CDTI). Spanish representative in the European Union Advisory Committee for Research (CREST).
- Oct 86-Dec 89 **Member, Technical Staff**, *MCC, Parallel Processing Program and ACA/Systems Technology Lab.; Austin, Texas.*
Principal Investigator, PAL project: parallel and sequential high performance implementation of next generation languages and systems. Development of theoretical bases, execution models, and actual implementations of high-performance parallel logic programming systems. Design and simulation of coherent-cache, cluster-based multiprocessors. Performance studies of execution models on several parallel architectures and organizations.

- Mar 87-Jan 90 **Assistant/Associate Professor** (adjunct), *University of Texas at Austin*, Computer Science Department.
Teaching (Computer Architecture, Logic Programming). Supervision of PhD. students working in the areas of parallel computer architecture, parallel execution model theory and design, abstract interpretation, and automatic parallelization.
- Jun 85-Aug 85 **Intern**, *MCC*; Austin, Texas.
- Jul 84-Aug 84 **Intern**, *IBM Corp.*; Austin, Texas.
- Nov 82-Aug 83 **Analyst**, *Philips S.A.*; Madrid, Spain.
Technical management.
- Jun 81-Aug 81 **Design Engineer** (praktikum), *Siemens GmbH.*; Munich, Germany.
Electronic circuit design and embedded systems development for audio and automotive applications.
- Jan 80-Oct 82 **President**, *HC Electronics*; Madrid, Spain.
Co-owned venture, involved in the development and licensing of electronic circuit designs and prototypes. Products included a Speech Scrambler marketed by Eurotronica S.A., and a Spectrum Scanning System installed at the National Optics Institute.

Other Selected Activities, Honors, and Awards

- “Julio Rey Pastor” **Prize for Research in Mathematics and Information and Communication Technologies**, 2006 (highest Spanish national scientific award in the areas of mathematics and information technology). “Aritmel” **Prize for Scientific Merit in Computer Science**, 2005. Elected member of the **Academia Europaea**.
- **Invited talks and/or tutorials** at RuleML’11, WLPE’11, TAPAS’10 (at SAS 2010), Datalog2.0 (2010), ProTest 09 workshop, 30 Years of Abstract Interpretation POPL’08 meeting, European Computer Science Summit’08, DAMP’07, EEAI’06, DAMP’06, ACM SAC’06, PPDP’05, EUROPAR’04, EU-IST GRIDS’04, SAS’03, ICALP’02, ERCIM/CoLogNet’02, LPAR’01, CL’00, CM-PP’00, AgentLink’99 WS, ICLP’99, AFLP’98, EUROPAR’97, ILPS’97, ICLP’97, PAAM / PAP’97, CCL’96, LPAR’94, JICSLP’92, SAS’92, IEEE CompEuro 92, ICLP’89 and at several Workshops and Summer Schools.
- **Associate Editor** of the “*Journal of New Generation Computing*” (Springer-Verlag), **Editorial Adviser** and former **Area Editor** (architecture and implementation) of “*Theory and Practice of Logic Programming*” (Cambridge U. Press) (formerly, the “*Journal of Logic Programming*”, Elsevier North-Holland), **Area Editor** of “*Journal of Applied Logic*” (Elsevier North-Holland), former **Associate Editor** of “*ACM Transactions on Programming Languages and Systems*” (ACM).
- **Program Committee Chairman or Co-Chairman**: “2010 International Conference on Logic Programming”, 2010, Edinburgh, UK, “Verification, Abstract Interpretation and Model Checking” VMCAI 2010, Madrid, Spain, “2008 Declarative Aspects of Multicore Programming” (San Francisco, CA, USA), “2008 International Symposium on Functional and Logic Programming (FLOPS’08) (Ise, Japan), “2005 Practical Aspects of Declarative Programming Symposium” PADL’05 (Long Beach, CA, USA), “2002 Static Analysis Symposium”, SAS’02 (Madrid, Spain), “Parallel Symbolic Computing” track of EUROPAR’97, 1997 (Passau, Germany); “Programming Languages: Implementations, Logics, and Programs”, 1994 (Madrid, Spain) and 1995 (Utrecht, The Netherlands); “North American Conference on Logic Programming”, 1990, Austin, TX.
- **Conference Chair or Co-Chair**, “ACM Principles of Programming Languages” POPL 2010, Madrid, Spain, “International Conference on Logic Programming” ICLP 2006, Seattle, WA, USA, “2005 Symposium on Practical Aspects of Declarative Languages” PADL’05, Long Beach, CA, USA, “2002 Static Analysis Symposium” SAS’02, Madrid, Spain, Joint “Algebraic and Logic

Programming” and *“Programming Languages: Implementations, Logics, and Programs”* conferences, 1994, Madrid, Spain, *First Spanish Conference on Declarative Programming*, PROLE’92, as well as a number of workshops since 1987.

- **Program Committee Member** for several conferences including PLDI’11, DAMP’11, ICLP’10, VMCAI’10, ICLP’08, SAS’08, ServiceWave’08, DAMP’08, FLOPS’08, TGC’07, SAS’07, TGC’06, FLOPS’06, PADL’06, ESOP’05, ICALP’05, PPSWR’05, AEPIA’05, PROLE’05, PADL’05, ICLP’04, EUMAS’04, PDCS’04, PROLE’04, PPDP’03, CP’03, PADL’03, EUMAS’03, PPDP’02, SAS’02, ICLP’01, SAS01, LOPSTR’01, POPL’00, LOPSTR’00, CMPP’00, ESOP’00, ECAI’00 CLPWS, PPDP’99, FLOPS’99, PADL’99, Irregular’99, POPL’97, ICLP’97, DPLE’97, CP’95, Europar’95, PLILP’94-95, PARLE’93, GULP-PRODE’92-94, PLILP’93-95, JICSLP’92, PAP’91, NACLP’90, ICLP’90, ICLP’89. **Session chairman** and **panelist** at several conferences and workshops.
- **Member** of ISTAG, the High-level Advisory Group of the Information Society Technologies Research Program (IST) of the European Union for Framework Program VI (2002-2006). President of several working groups. Co-authored several “white books” and policy reports (not included in publications list).
- **Panel Member** ACM 50th Anniversary Workshop on Future Directions in Computer Science, the ALP Strategic Directions Meeting, and at several EU Vth, VIth, and VIIth Framework Program strategic planning meetings (“New Directions for ICTs in FP7 meeting: Grand challenges for basic research,” “Next Generation GRIDs,” “Global Computing,” “Future Software Systems,” “Advanced Embedded Systems,” etc.).
- **Invited speaker** at the Danish EU presidency high-level meeting on the need for a European Research Council.
- **Acted as scientific expert**, at Spanish Senate hearings, on software patents and open source software. Expert and National Representative for the OECD on Dissemination of Scientific Data and Results.
- **Elected President** of the *Association for Logic Programming* (ALP) and the *Spanish Research Consortium in Informatics and Mathematics* (SpaRCIM, Spain’s member in ERCIM).
- **Elected member of the steering boards** of the *European Association for Programming Languages and Systems* (EAPLS), and (twice) of the ALP. Member of the Steering Committee of *ACM Principles of Programming Languages Conference* (POPL) (and POPL’2011 Most Influential Paper Award committee), *Verification, Abstract Interpretation and Model Checking Conference* (VMCAI), *Static Analysis Symposium* (SAS), *Functional and Logic Programming Symposium* (FLOPS), *ACM Principles and Practice of Declarative Programming Symposium* (PPDP), *Federated Logic Conference* (FLoC), and *Declarative Aspects of Multicore Programming Workshop* series.
- **Member of the Scientific Advisory Board** of the Spanish *National Research Council*, *ACM SIGLOG*, *IFCoLog*, and the *Initiative de Recherche et Innovation sur le Logiciel Libre* Center.
- **Best paper award**, at ICLP 2005 (with José Morales, Manuel Carro, and Germán Puebla).
- **Member of the winning team** at the 18th Prolog Programming contest, at ICLP 2011 in Lexington KY (with Michael Leuschel and Antonio Porto).
- Served on **tenure and promotion committees** or as a **department evaluator** for the K.U.Leuven (Belgium, University of Copenhagen (DIKU), T.U. Denmark (TUD), Cambridge U. (U.K.), Cyprus U., INRIA (France), Ecole Normale Supérieure (France), U. of Porto (Portugal), U. of Melbourne (Australia), New Mexico State U. (USA), Ecole Polytechnique (France), Negev U. (Israel), etc. and several Spanish universities. **President** of the Spanish national **research performance evaluation commission** for the areas of Computer Science and Electronics (CNEAI). Member of Department Evaluation Advisory Board at *Informatics Europe*.
- **Area Coordinator** (parallelism and implementation technologies) of the EU Networks of Excellence in Computational Logic COMPULOG and CoLogNet.

- **Invited Lecturer**, October 1996, in the distinguished visitor lecture series of New Mexico State University. Former **lecturer** on the “*IEEE Computer Society Distinguished Visitor Program*”.
- **Invited Scientist**, August-September 1988, at the Japanese Institute for fifth generation computing (ICOT) in Tokyo.
- **Developed a number of practical systems**, which are in the public domain, some of them with thousands of users, including the Ciao programming system, CiaoPP, LPdoc, the PiLLoW library, CP analyzers, PLAI, automatic parallelizers, &-Prolog, VisAndOr, etc.
- Participated in more than 50 **research projects**, in most of them as principal investigator, funded by the European Union and the Spanish Ministries, as well as NSF, MITI, etc. Also in several projects funded by industry.
- **Referee** for several journals (TOPLAS, Computing Reviews, TCS, TPLP/JLP, JSC, JPDC, FGCS, TODS, LOPLAS, IJPP, IEEE-TSE, JFGC, COMPLANG, JFLP, etc.), and conferences (POPL, SAS, IJCAI, ICLP, SLP, ILPS, PPDP, NAACP, JICSLP, ICPP, DCS, CONPAR, CP, ESOP, EUMAS, PAP, PARLE, PLILP, ALP, POPL, WSA, EUROPAR, GULP, ICALP, ICPA, PODS, ASIAN, CAEPIA, PACT, PASCO, PPSWR, etc.).
- **Evaluator** and panel member for several institutions: EU (ESPRIT, IST FP IV, V, VI), NSF, DARPA, DoD, ICOT, ISF, FULBRIGHT, the Australian, Belgian, Danish, Swedish, Spanish, and UK funding agencies, etc.
- **Faculty Adviser** for the ACM student chapter of the T.U. of Madrid.
- **Fulbright-Hays Scholarship** for Graduate Study in the U.S. (1983-84). **U. of Texas Graduate Research Grant** (1984-85). **MCC Graduate Research Grant** (1985-86).
- **Professional Societies**: ACM, IEEE (Computer Society), ALP (Assoc. for Logic Programming), EAPLS, ATI (Professional Computer Engineers' Assoc. of Spain), ERCIM/SpaRCIM, Informatics Europe, among others.
- **Member** of the Tau Beta Pi Engineering Honor Society.

Students and Recent Collaborators

Graduated Ph.D. Students (+ graduating university/current location):

Kalyan Muthukumar (U.T. Austin/IBM San Jose / Apple research), Yow-Yan Lin (UT Austin/Bellcore), María García de la Banda (T.U. Madrid/Melbourne University), Francisco Bueno (T.U. Madrid), Germán Puebla (T.U. Madrid), Pedro López García (T.U. Madrid), Manuel Carro (T.U. Madrid), Daniel Cabeza Gras (T.U. Madrid), Jorge Navas (U. of New Mexico/U. of Singapore), Amadeo Casas (U. of New Mexico/Microsoft), Mario Méndez (U. of New Mexico/U.T. Austin), José Francisco Morales (T.U. Madrid/Complutense U.).

Most recent collaborators (outside the IMDEA SW Institute and UPM):

P. Stuckey (Melbourne U., Australia), K. Marriott (Monash U., Australia), E. Pontelli (New Mexico S.U., USA), G. Gupta (New Mexico S.U., USA), S. Debray (U. of Arizona, USA), U. Montanari (U. Pisa, Italy), F. Rossi (U. Pisa, Italy), K. Shen (Imperial College, UK), M. Codish (Ben Gurion U., Israel), M. Bruynooghe (K.U. Leuven, Belgium), P. Deransart (INRIA, France), J. Maluszynski (Linköping U., Sweden), V. Santos-Costa (U. of Porto, Portugal), J. Gallagher (U. of Roskilde, Denmark), D. Kapur (U. of New Mexico, USA), D. Stefanovic (U. of New Mexico, USA).

Personal data / contact

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Publications (in reverse chronological order, and classified by venue category)

Publications are classified according to four ranking databases:

- the JCR listings (using for each publication venue the average position in the list in 1998-2008)
- the CORE listings, and
- the CiteSeer impact listings (see also the upgraded CiteSeerX listing).

Each of these databases (except CORE) maps venues to a number between 0 and 1 (or 0 and 100%) which corresponds to the position of the corresponding venue divided by the total number of ranked venues (the lower the position the better). CORE classifies venues, instead, into four discrete ranking categories: A+ (or A*), A, B and C. In order to have a numerical figure with which to compare to the other databases and be able to compute an average value, we have mapped CORE conference rankings A to 33.0%, B to 64.0% and C to 100.0%, and CORE journal rankings A* to top 5%, A to 20%, B to 64% and C to 100%. We obtain an overall numerical ranking for each publication as the **average** of all *available* rankings for the corresponding venue (some venues do not appear in all ranking databases). Finally, publications are classified according to this average. Publications with average ranking 0-33% are considered as **first level**, 33-66% are considered as **second level**, and the rest are considered as **third level**. In the listings, for each publication we report the individual rankings available for the corresponding venue, as well as the global average position, in the form of a percentage.

Articles in First-Level Refereed Conferences and Journals

1. M. V. Hermenegildo, F. Bueno, M. Carro, P. López, E. Mera, J.F. Morales, G. Puebla. *An Overview of Ciao and its Design Philosophy*. Theory and Practice of Logic Programming, To Appear, Cambridge University Press, 2012. <http://arxiv.org/abs/1102.5497>.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
2. G. Puebla, E. Albert, M. Hermenegildo. *Efficient Local Unfolding with Ancestor Stacks*. Theory and Practice of Logic Programming, Vol. 11, Num. 1, pages 1–32, Cambridge U. Press, January 2011.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
3. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Reduced Certificates in Abstraction-Carrying Code*. Theory and Practice of Logic Programming, To Appear, 2011.

JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.

4. D. Cabeza, M. Hermenegildo. *Non-Strict Independence-Based Program Parallelization Using Sharing and Freeness Information*. Theoretical Computer Science, Vol. 46, Num. 410, pages 4704–4723, Elsevier Science, October 2009.
JCR: position (ave) top 59%, impact (ave) 0.60, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 338/1221 (top 28%), impact 0.91. Average position: top 36%.
5. E. Mera, P. López-García, M. Hermenegildo. *Integrating Software Testing and Run-Time Checking in an Assertion Verification Framework*. 25th International Conference on Logic Programming (ICLP'09), LNCS, Num. 5649, pages 281–295, Springer-Verlag, July 2009.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
6. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *A Tabling Implementation Based on Variables with Multiple Bindings*. International Conference on Logic Programming (ICLP 2009), LNCS, Num. 5649, 15 pages, Springer-Verlag, July 2009.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
7. M. Marron, D. Kapur, M. Hermenegildo. *Identification of Logically Related Heap Regions*. ISMM'09: Proceedings of the 8th international symposium on Memory management, ACM Press, June 2009.
CORE: A. Citeseer: position 99/1221 (top 8%), impact 1.55. Average position: top 21%.
8. E. Trias, J. Navas, E. S. Ackley, S. Forrest, M. Hermenegildo. *Negative Ternary Set-Sharing*. International Conference on Logic Programming, ICLP, LNCS, Num. 5366, pages 301–316, Springer-Verlag, December 2008.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
9. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *A Sketch of a Complete Scheme for Tabled Execution Based on Program Transformation*. Int'l. Conference on Logic Programming, LNCS, Vol. 5366, pages 795–800, Springer Verlag, December 2008. Short paper.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
10. A. Casas, M. Carro, M. Hermenegildo. *A High-Level Implementation of Non-Deterministic, Unrestricted, Independent And-Parallelism*. 24th International Conference on Logic Programming (ICLP'08), LNCS, Vol. 5366, pages 651–666, Springer-Verlag, December 2008.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
11. M. Marron, M. Hermenegildo, D. Kapur, D. Stefanovic. *Efficient Context-Sensitive Shape Analysis with Graph-Based Heap Models*. International Conference on Compiler Construction (CC 2008), Lecture Notes in Computer Science, 15 pages, Springer, April 2008.
CORE: A. CiteseerX position (ave) top 26%, impact (ave) 0.271. Average position: top 30%.
12. P. Pietrzak, M. Hermenegildo. *Automatic Binding-related Error Diagnosis in Logic Programs*. International Conference on Logic Programming (ICLP'07), LNCS, Num. 4670, pages 333–347, Springer-Verlag, September 2007.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
13. J. Navas, E. Mera, P. López-García, M. Hermenegildo. *User-Definable Resource Bounds Analysis for Logic Programs*. 23rd International Conference on Logic Programming (ICLP'07), Lecture Notes in Computer Science, Vol. 4670, Springer, 2007.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

14. M. Marron, D. Kapur, D. Stefanovic, M. Hermenegildo. *A Static Heap Analysis for Shape and Connectivity*. Languages and Compilers for Parallel Computing (LCPC'06), Lecture Notes in Computer Science, Vol. 4382, pages 345–363, Springer, November 2006.
Citeseer: position 186/1221 (top 15%), impact 1.22. Average position: top 15%.
15. M. Carro, J. Morales, H.L. Muller, G. Puebla, M. Hermenegildo. *High-Level Languages for Small Devices: A Case Study*. Compilers, Architecture, and Synthesis for Embedded Systems, pages 271–281, ACM Press / Sheridan, October 2006.
CORE: A. CiteseerX position (ave) top 23%, impact (ave) 0.192. Average position: top 28%.
16. E. Mera, P. López-García, G. Puebla, M. Carro, M. Hermenegildo. *Using Combined Static Analysis and Profiling for Logic Program Execution Time Estimation*. 22nd International Conference on Logic Programming (ICLP'06), LNCS, Num. 4079, pages 431–432, Springer-Verlag, August 2006.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
17. G. Puebla, E. Albert, M. Hermenegildo. *Abstract Interpretation with Specialized Definitions*. The 13th International Static Analysis Symposium (SAS'06), LNCS, Num. 4134, pages 107–126, Springer, August 2006.
CORE: A. Citeseer: position 145/1221 (top 12%), impact 1.35. Average position: top 22%.
18. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Reduced Certificates for Abstraction-Carrying Code*. 22nd International Conference on Logic Programming (ICLP 2006), LNCS, Num. 4079, pages 163–178, Springer-Verlag, August 2006.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
19. J. Morales, M. Carro, G. Puebla, M. Hermenegildo. *A Generator of Efficient Abstract Machine Implementations and its Application to Emulator Minimization*. International Conference on Logic Programming, LNCS, Num. 3668, pages 21–36, Springer Verlag, October 2005.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
20. G. Puebla, E. Albert, M. Hermenegildo. *A Generic Framework for the Analysis and Specialization of Logic Programs*. International Conference on Logic Programming (ICLP 2005), LNCS, Num. 3668, pages 407–409, Springer, October 2005. Extended Abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
21. M. Hermenegildo, G. Puebla, F. Bueno, P. López-García. *Integrated Program Debugging, Verification, and Optimization Using Abstract Interpretation (and The Ciao System Preprocessor)*. Science of Computer Programming, Vol. 58, Num. 1–2, pages 115–140, Elsevier Science, 2005.
JCR: position (ave) top 42%, impact (ave) 0.80, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A. Citeseer: position 185/1221 (top 15%), impact 1.22. Average position: top 26%.
22. E. Albert, G. Puebla, M. Hermenegildo. *Abstract Interpretation-based Mobile Code Certification*. Proc. of International Conference on Logic Programming (ICLP'04), LNCS, Num. 3132, pages 446–447, Springer-Verlag, September 2004. Extended Abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
23. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Persistence Model for CLP Systems*. 2003 International Conference on Logic Programming, LNCS, Num. 2916, pages 481–482, Springer-Verlag, December 2003. Extended abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

24. S. Muñoz, J.J. Moreno-Navarro, M. Hermenegildo. *Efficient Negation Using Abstract Interpretation*. Proc. of the Eighth International Conference on Logic Programming and Automated Reasoning, LNAI, 15 pages, Springer-Verlag, December 2001.
CORE: A. CiteseerX position (ave) top 38%, impact (ave) 0.169. Average position: top 36%.
25. G. Gupta, E. Pontelli, K. Ali, M. Carlsson, M. Hermenegildo. *Parallel Execution of Prolog Programs: a Survey*. ACM Transactions on Programming Languages and Systems, Vol. 23, Num. 4, pages 472–602, ACM Press, July 2001.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
26. D. Cabeza, M. Hermenegildo. *Distributed WWW Programming using (Ciao) Prolog and the PiLLoW Library*. Theory and Practice of Logic Programming, Vol. 1, Num. 3, pages 251–282, Cambridge U. Press, May 2001.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
27. M. Hermenegildo. *Parallelizing Irregular and Pointer-Based Computations Automatically: Perspectives from Logic and Constraint Programming*. Parallel Computing, Vol. 26, Num. 13–14, pages 1685–1708, Elsevier Science, December 2000.
JCR: position (ave) top 46%, impact (ave) 0.75, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 519/1221 (top 43%), impact 0.63. Average position: top 36%.
28. D. Cabeza, M. Hermenegildo. *A New Module System for Prolog*. International Conference on Computational Logic, CL2000, LNAI, Num. 1861, pages 131–148, Springer-Verlag, July 2000.
CiteseerX position (ave) top 28%, impact (ave) 0.205. Average position: top 28%.
29. M. Hermenegildo. *A Documentation Generator for (C)LP Systems*. International Conference on Computational Logic, CL2000, LNAI, Num. 1861, pages 1345–1361, Springer-Verlag, July 2000.
CiteseerX position (ave) top 28%, impact (ave) 0.205. Average position: top 28%.
30. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in CLP Languages*. ACM Transactions on Programming Languages and Systems, Vol. 22, Num. 2, pages 269–339, ACM Press, March 2000.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
31. M. Hermenegildo, G. Puebla, K. Marriott, P. Stuckey. *Incremental Analysis of Constraint Logic Programs*. ACM Transactions on Programming Languages and Systems, Vol. 22, Num. 2, pages 187–223, ACM Press, March 2000.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
32. M. Carro, M. Hermenegildo. *Concurrency in Prolog Using Threads and a Shared Database*. 1999 International Conference on Logic Programming, pages 320–334, MIT Press, Cambridge, MA, USA, November 1999.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

33. G. Puebla, M. Hermenegildo. *Abstract Multiple Specialization and its Application to Program Parallelization*. J. of Logic Programming. Special Issue on Synthesis, Transformation and Analysis of Logic Programs, Vol. 41, Num. 2&3, pages 279–316, Elsevier - North Holland, November 1999.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
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